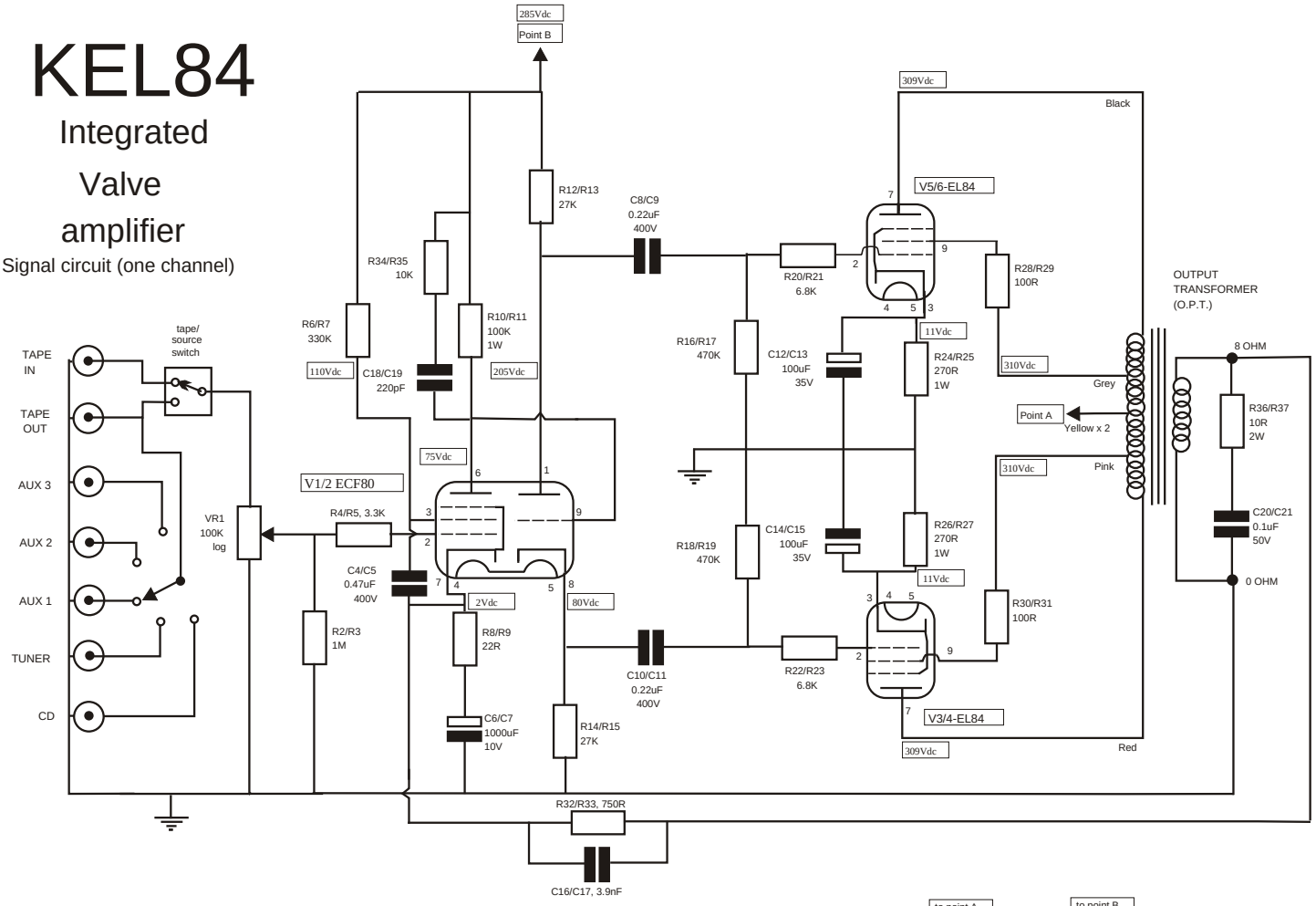
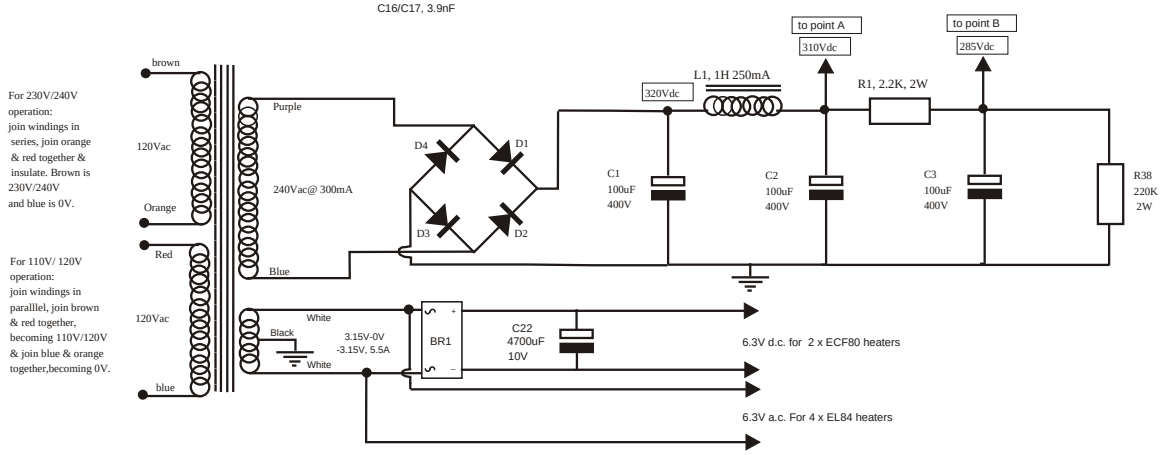
KEL84

Integrated Valve amplifier

Signal circuit (one channel)



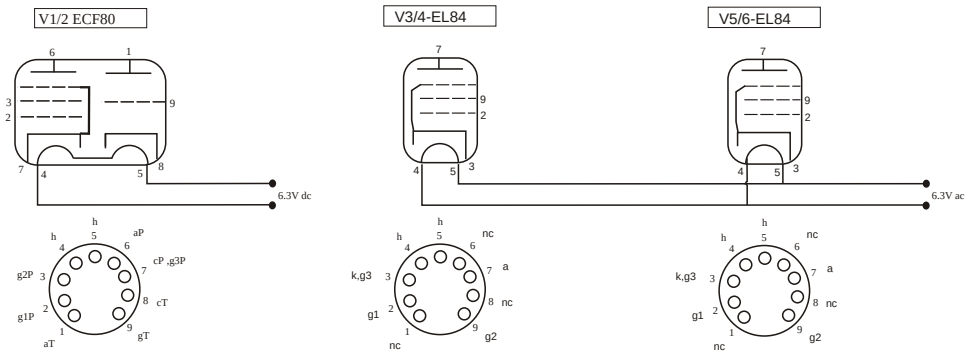
Power supply unit (both channels)



For 230V/240V operation: join windings in series, join orange & red together & insulate. Brown is 230V/240V and blue is 0V.

For 110V/ 120V operation: join windings in paralllel, join brown & red together, becoming 110V/120V & join blue & orange together,becoming 0V.

VALVE PIN LAYOUT

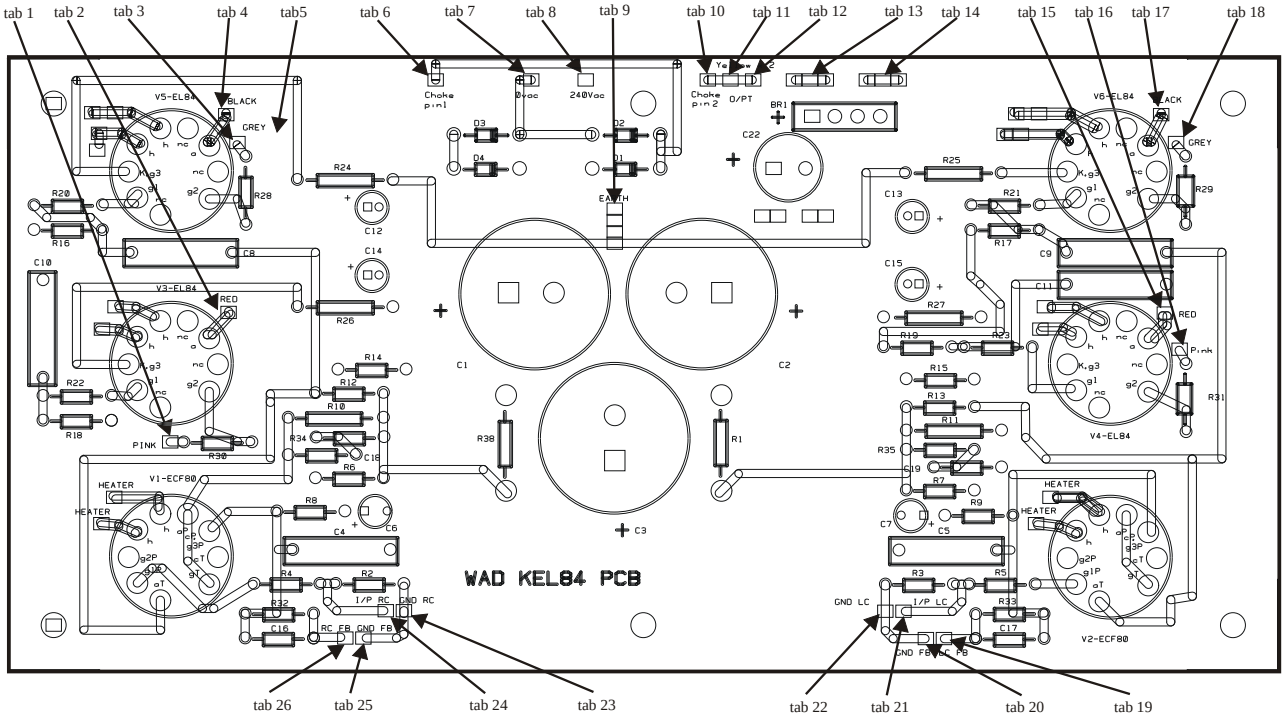


Views are from underneath valve or valve holder

h = heater hct = heater centre tap c = cathode a = anode g1= grid 1 g2 = grid 2 g3 = grid 3 nc = no connection (T=triode P=pentode for V1)

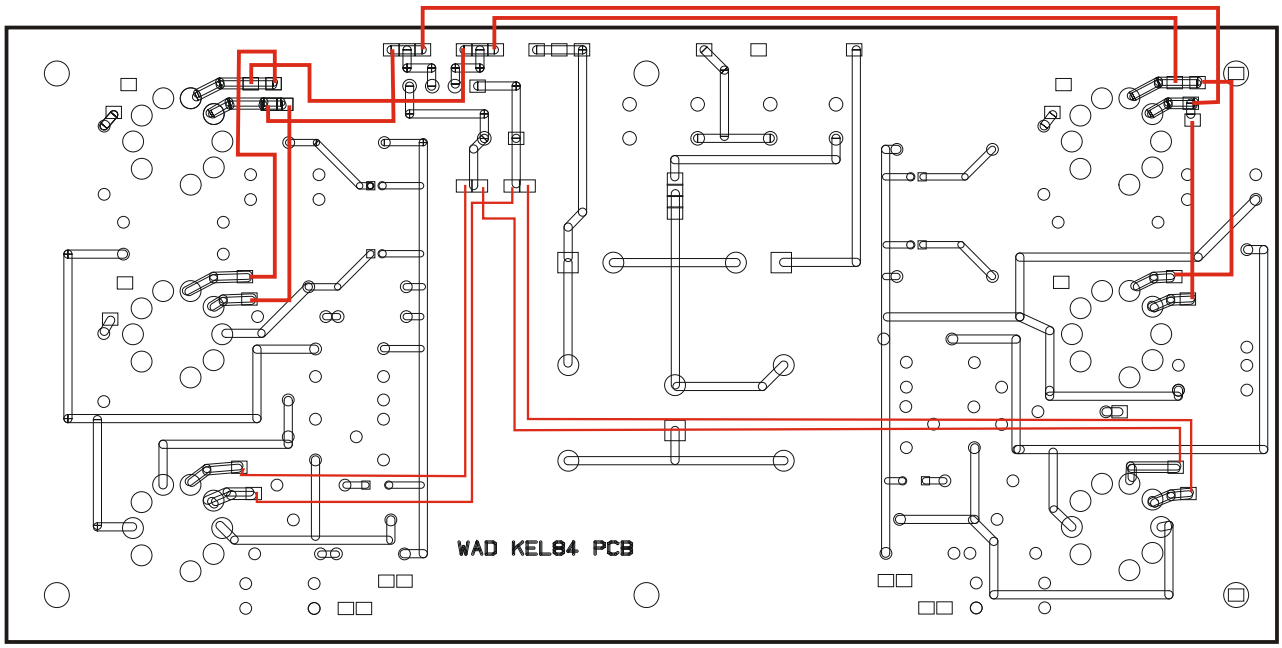
PRINTED CIRCUIT BOARD TAB DIAGRAM

Diagram shows the position of all tabs (1 - 27) that have connections external to the printed circuit board. At these tab points insert the terminal pins provided and solder both sides of the board. Insure they project out onto the component side, because once the PCB is fitted into position it will ease all external soldering. Please note that the view shown is component side.



PRINTED CIRCUIT BOARD HEATER DIAGRAM

Diagram shows the position and route of all the valve heater wiring. Please note that the view is of the valve base side. For the sake of clarity, straight lines are used in the diagram when in reality each pair of valve heater wires should be twisted. Where the heater lines cross do not join them. For each wire end thread the wire through the hole and solder both sides. There is no need for terminal pins here



KEY:
- VALVE BASE SIDE - follows the route of all the solder tracks that are on the valve base side.
- Wire heater links used for V1 & V2 USE BLACK 1/0.6 wire provided.
- Wire heater links used for V3, V4, V5 & V6 USE BLACK 1/1.2 wire provided

FIG.1 IEC MAINS INPUT SOCKET & POWER SWITCH
(REAR VIEW)

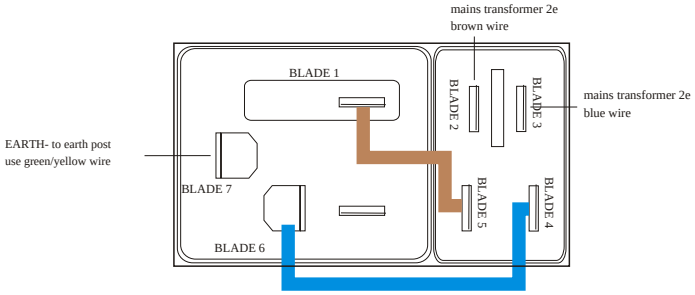


FIG. 2. Br1 HEATER
BRIDGE RECTIFIER

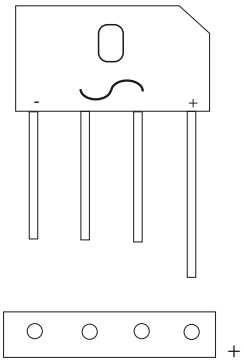
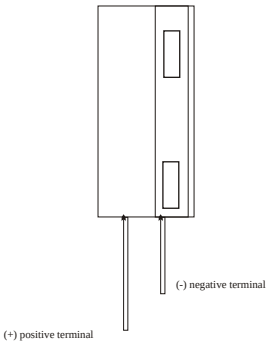
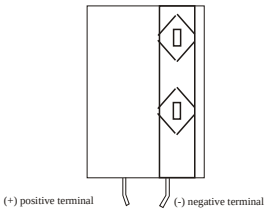


FIG. 3. LAYOUT AND ORIENTATION OF THE ELECTROLYTIC CAPACITORS AND DIODES

POLARITY MARKINGS
FOR C6/7/12/13/14/15/22



POLARITY MARKINGS
FOR C1/2/3



POLARITY MARKINGS
FOR D1,2,3 & 4

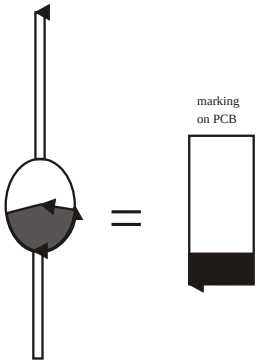
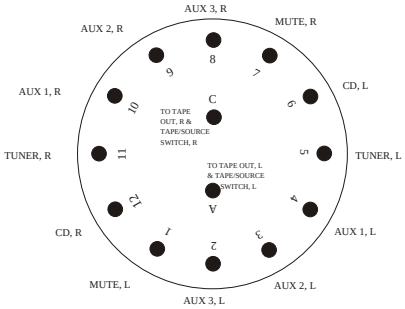


FIG. 4 SHOWS THE INPUT SELECTOR SWITCH

REAR VIEW

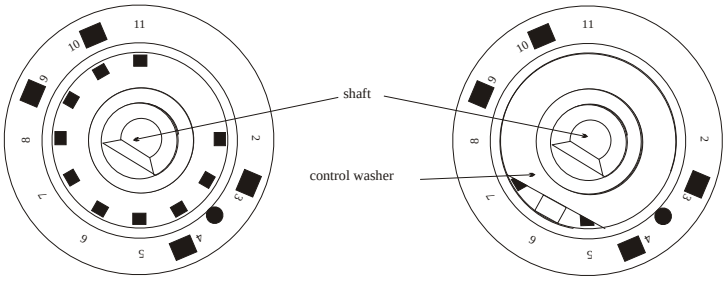
(SHOWS SELECTOR TO PHONO SOCKET CONNECTIONS)



L = left channel R = Right channel

FRONT VIEW

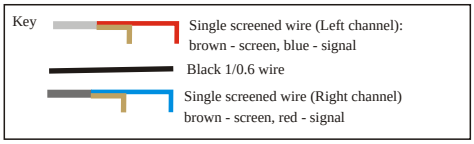
(SHOWS ORIENTATION OF SELECTOR SWITCH CONTROL WASHER TO 6/2 POLE POSITION)



Selector with control washer removed

Selector with control washer
fixed to 6/2 pole position.

FIG. 5.. SHOWS LAYOUT OF INPUT SIGNAL WIRING
This is a diagrammatical view of the interior of the Kel84.
Keep all signal wires bunched together. Where wires meet
at a “T” junction they are joined, wires that cross are not.



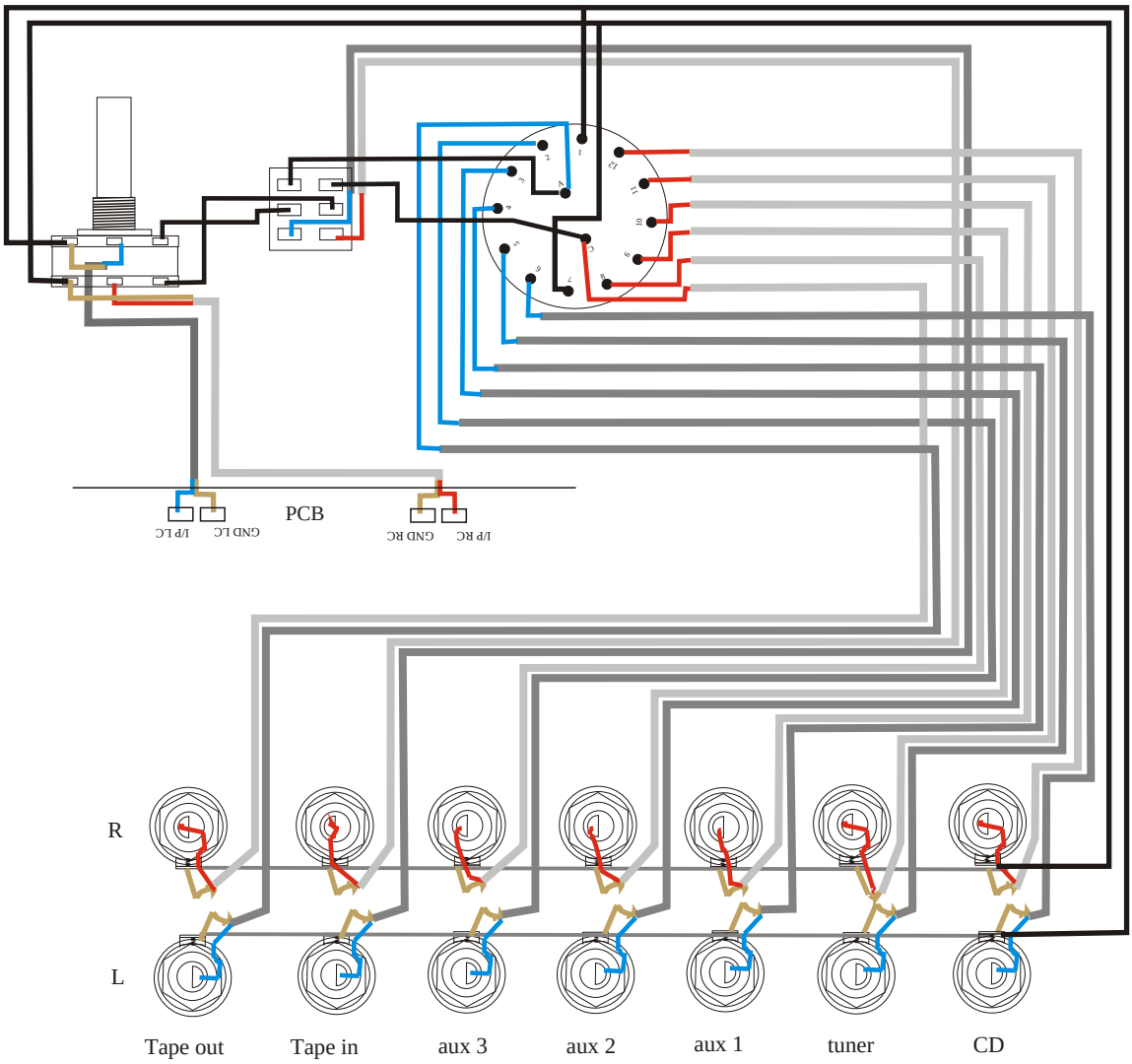


FIG. 6. VR1 100k DUAL
LOG POTENTIOMETER

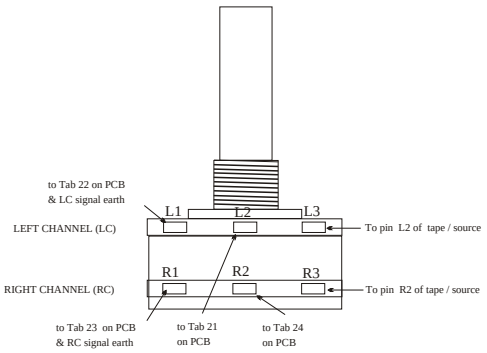


FIG.7. TAPE / SOURCE SWITCH

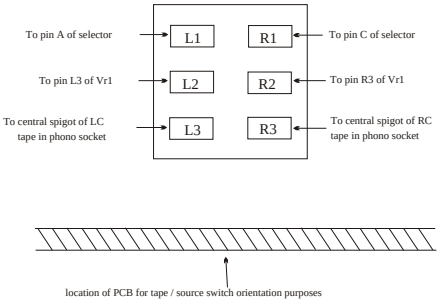


FIG. 8. EXPLODED VIEW OF HOW TO FIT THE PHONO SOCKETS

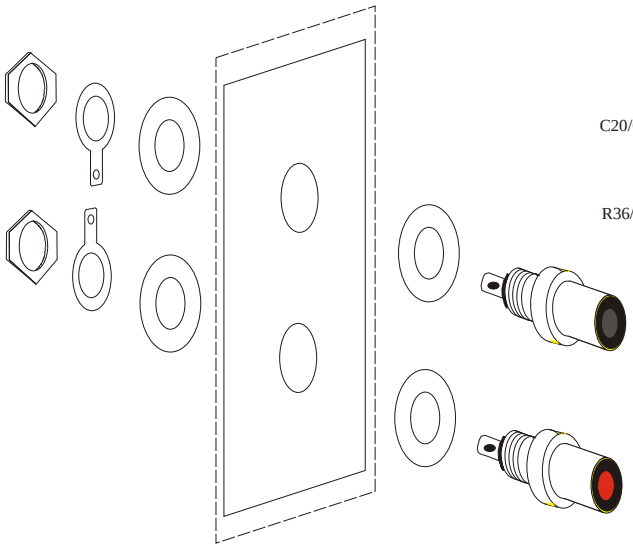


FIG. 9. EXPLODED VIEW OF HOW TO FIT THE BINDING POSTS

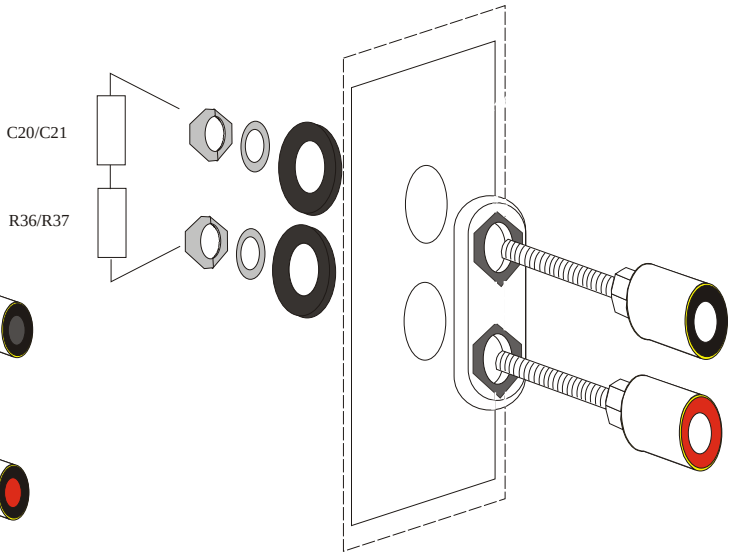


FIG.10. INTERIOR VIEW ILLUSTRATING ORIENTATION OF THE TRANSFORMERS.