

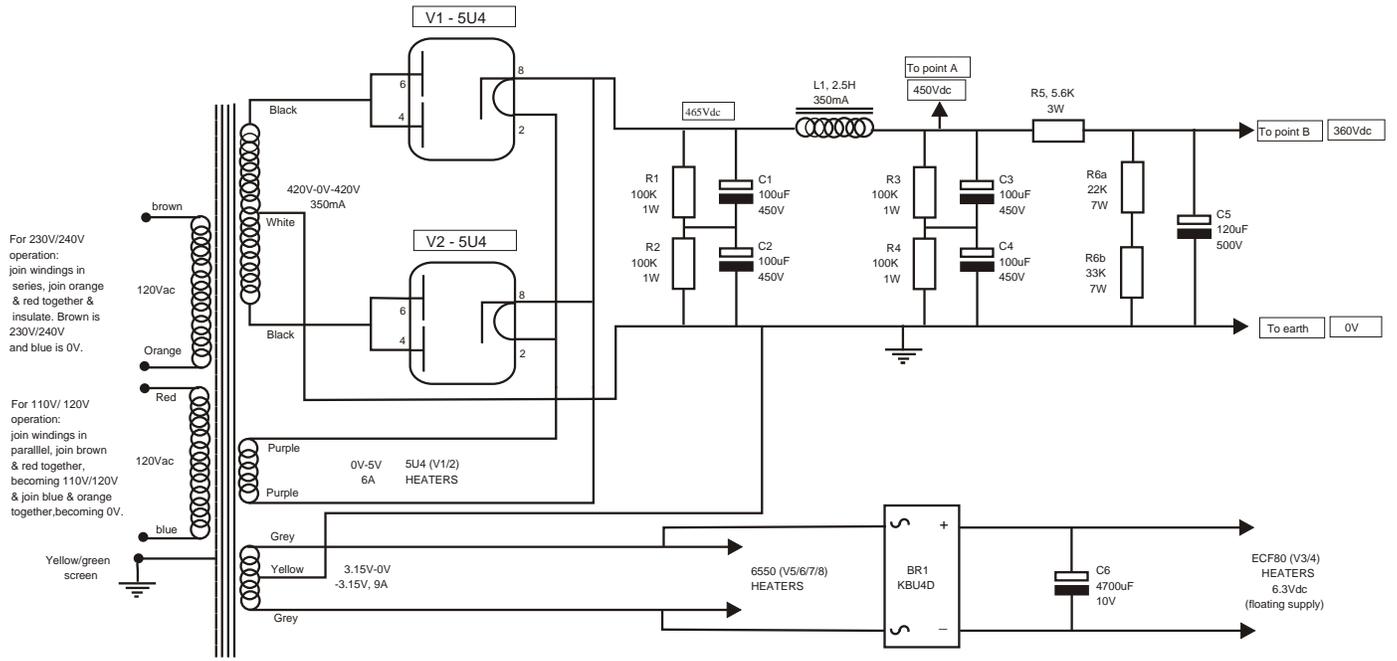
DIAGRAM ONLY
KIT6550 INTEGRATED
AMPLIFIER KIT
INSTRUCTIONS

KiT6550

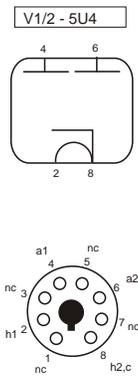
Integrated Valve Amplifier

Circuit Diagram

power supply circuit (both channels)



Valve pin layout



Views are from underneath valve or valve holder
h1, h2 = heater a1, a2 = anode c = cathode nc = no connection

DIAGRAM SHOWS KIT6550 TAG BOARD LAYOUT

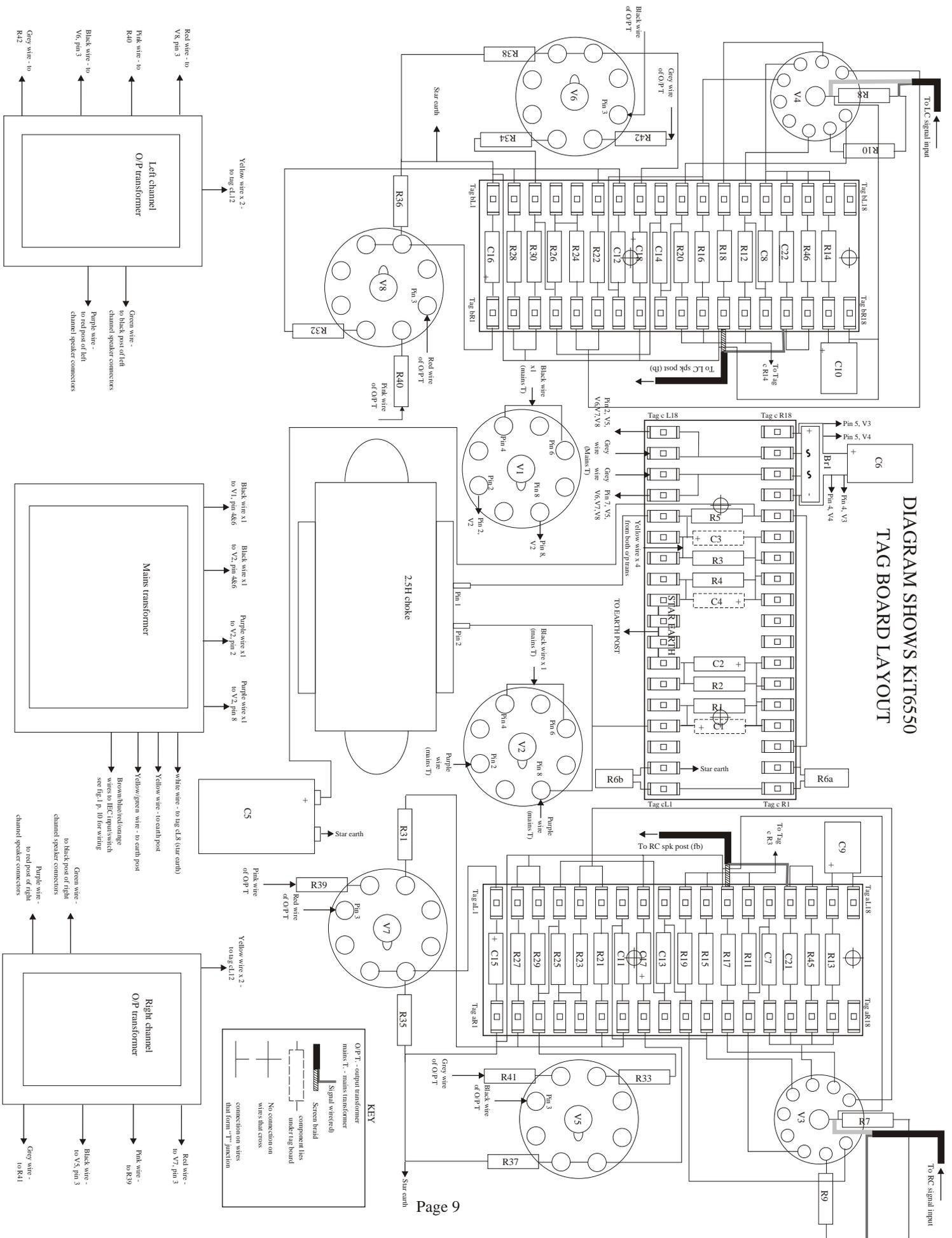


FIG.1 IEC MAINS INPUT SOCKET & POWER SWITCH
(REAR VIEW)

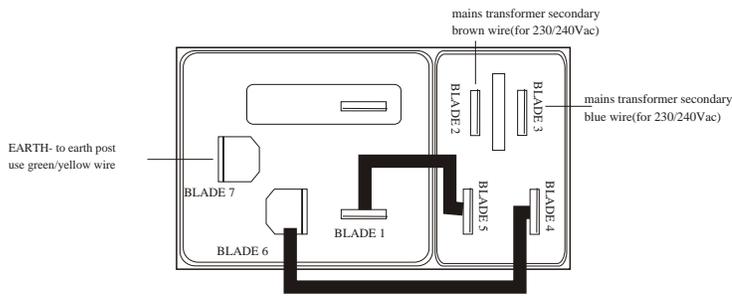


FIG. 2. Br1 HEATER
BRIDGE RECTIFIER

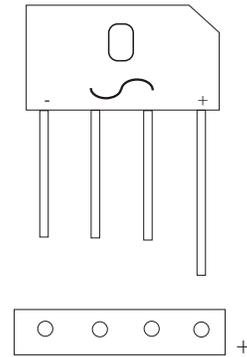
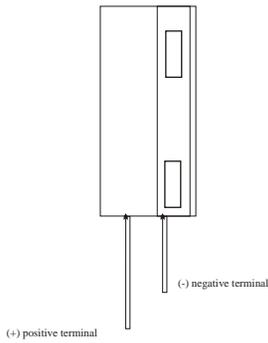
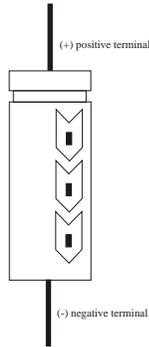


FIG. 3. LAYOUT AND ORIENTATION OF THE ELECTROLYTIC CAPACITORS AND DIODES

POLARITY MARKINGS
FOR C6, C9, C10, C16, C17, C18 & C19



POLARITY MARKINGS FOR
C1, C2, C3 & C4



POLARITY MARKINGS
FOR C5

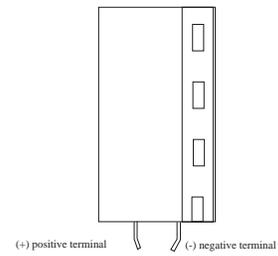
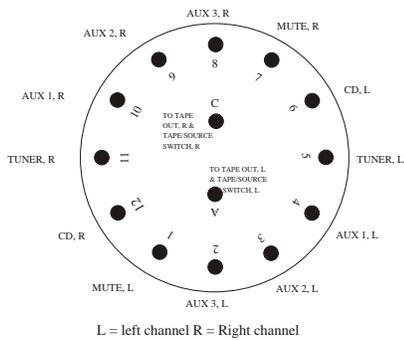


FIG. 4 SHOWS THE INPUT SELECTOR SWITCH

REAR VIEW

(SHOWS SELECTOR TO PHONO SOCKET CONNECTIONS)



FRONT VIEW

(SHOWS ORIENTATION OF SELECTOR SWITCH CONTROL WASHER TO 6/2 POLE POSITION)

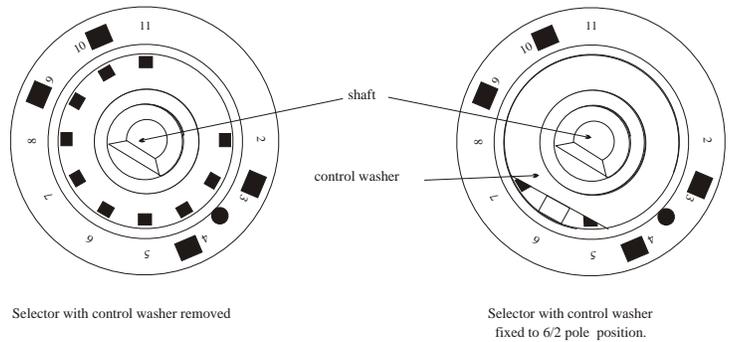


FIG. 5.. SHOWS LAYOUT OF INPUT SIGNAL WIRING
 This is a diagrammatical view of the interior of the Kel84.
 Keep all signal wires bunched together. Where wires meet at a "T" junction they are joined, wires that cross are not.

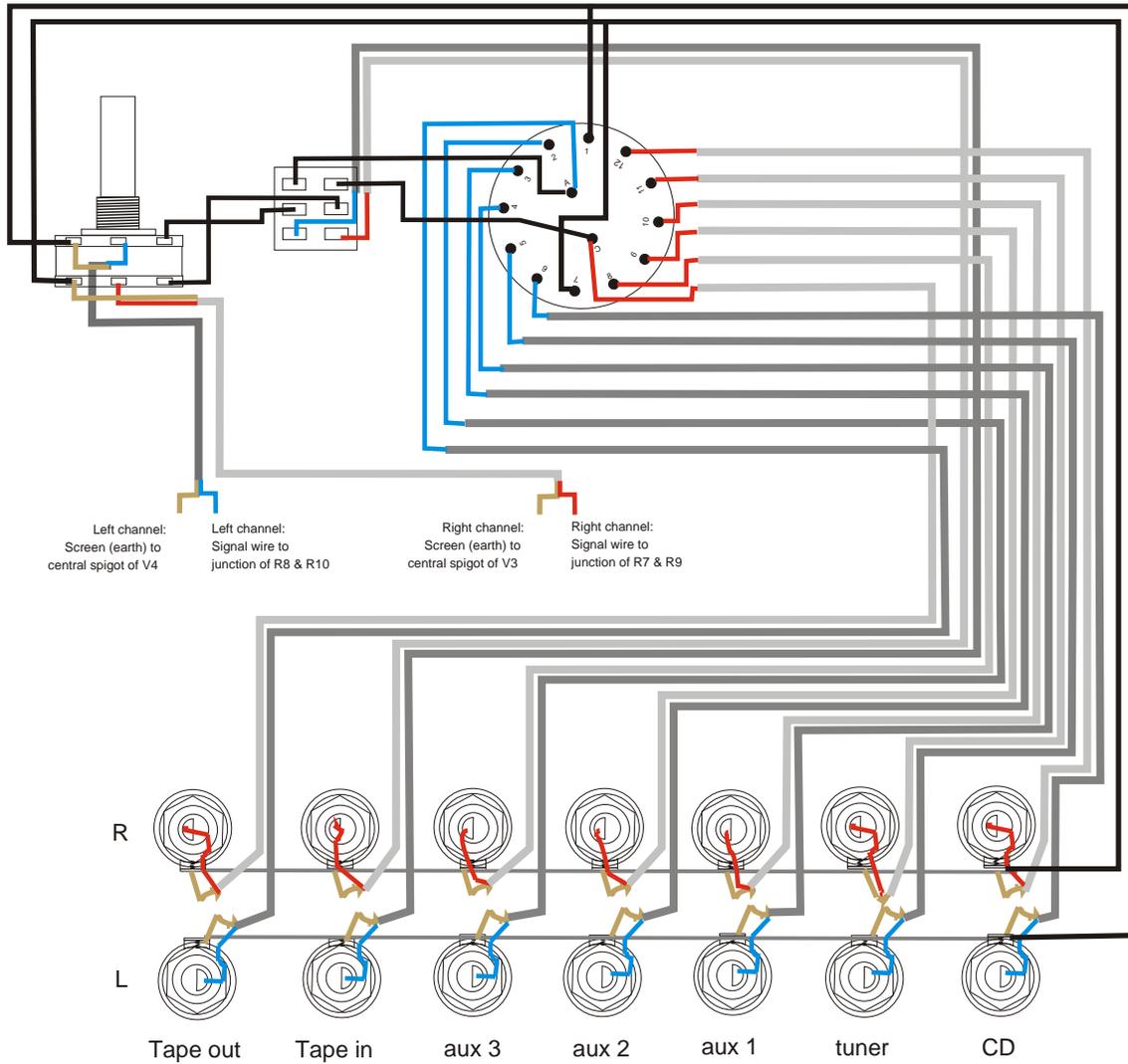
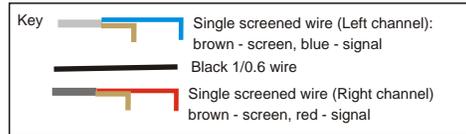


FIG. 6. VR1 100k DUAL LOG POTENTIOMETER

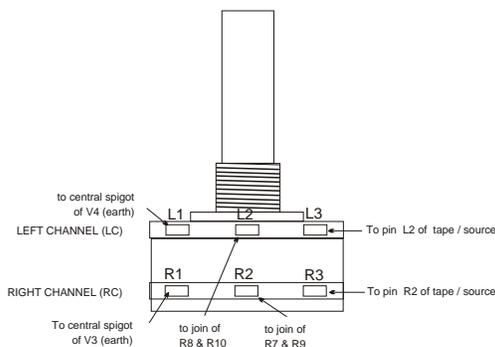


FIG.7. TAPE / SOURCE SWITCH

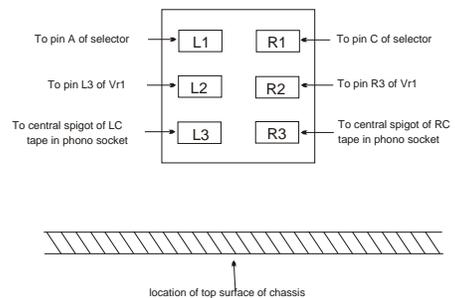


FIG. 8. EXPLODED VIEW OF HOW TO FIT THE PHONO SOCKETS

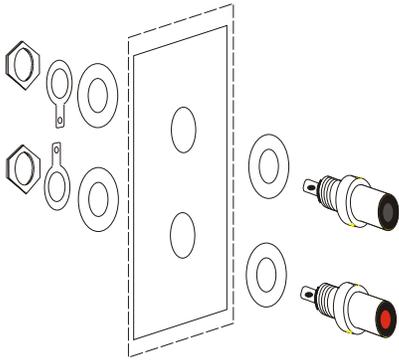


FIG. 9. VIEW OF HOW TO FIT THE MAINS & OUTPUT TRANSFORMER

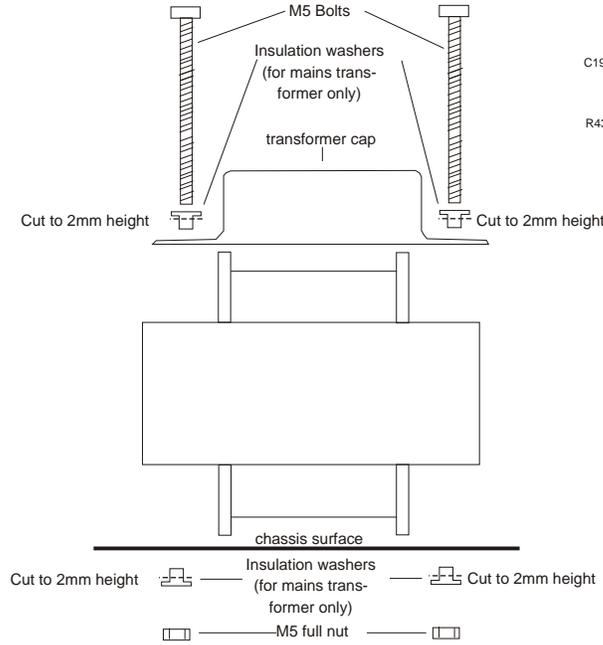


FIG. 10. EXPLODED VIEW OF HOW TO FIT THE BINDING POSTS

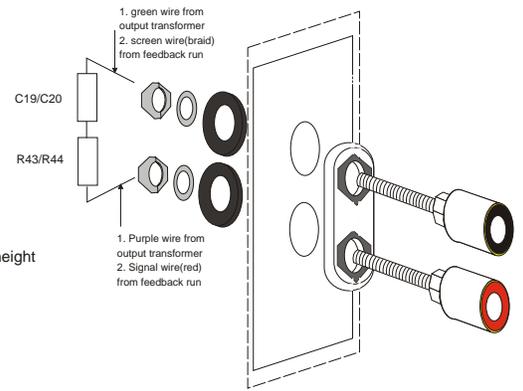


FIG. 11 DIAGRAM SHOWS THE WIRING ROUTES. PLEASE NOTE THAT THIS IS USED IN CONJUNCTION WITH PAGE 9 AS ALL NON-ESSENTIAL OFF TAG BOARD COMPONENTS AND LINKS HAVE BEEN REMOVED. IF LINKS ARE NOT RE-PRESENTED THEN THEY ARE TOO SMALL TO WARRANT CONSIDERATION AND YOU SHOULD TAKE THE DIRECT ROUTE. THE COLOURS USED DO NOT RELATE TO THE WIRE COLOURS.

